IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Aprilicant : Ohtani et al

Art Unit: 1765

Serial No.: 08/690,747

Examiner: Robert Funemund

Filed

: August 1, 1996

Title : METHOD !

: METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE

Commissioner for Fatents Washington, D.C. 20231

AMENDMENT

In response to the action mailed February 15, 2000, please amend the application as follows:

In the Claims:

Please amend claims 1, 6, 22, 23, 26, 27, and 28 as follows:

1. (Amerided) A method for manufacturing a semiconductor device comprising the steps of:

forming a non-single crystalline semiconductor film [to become at least a channel forming region] on an insulating surface;

patterning said semiconductor film into a patterned semiconductor film having peripheral portions;

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I neteby certify inder 37 CFR \$1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 19231.

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